IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/069,670 Examiner: Swearingen, Jeffrey

Applicant: Leroy, Xavier Group Art Unit: 3622
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A METHOD FOR TRANSFORMING AND VERIFYING DOWNLOADED PROGRAM FRAGMENTS WITH DATA TYPES RESTRICTIONS AND

CORRESPONDING SYSTEM

Title:

DECLARATION UNDER 37 CFR 1.132

I, the undersigned, Seth A. Milman, a Patent Agent at Holland & Knight LLP in Boston, Massachusetts, do hereby declare that:

- Being hereby warned that willful false statements are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any resulting Letters Patent issuing thereon, I state that all statements made of my own knowledge are true and all statements made on information and belief are believed to be true.
- Exhibit A, attached to this affidavit, is a true and accurate copy of the Preface section and Section 1.3 of the Java Card 2.1 Virtual Machine Specification, published June 7, 1999 by Sun Microsystems.
- Exhibit B, attached to this affidavit, is a true and accurate copy of Section 4.9 of the The Java Virtual Machine Specification", by Tim Lindholm and Frank Yellin (Addison-Wesley, 1996), and also available online at http://java.sun.com/docs/books/jvms/first_edition/html/VMSpecTOC.doc.html.
- Exhibit C, attached to this affidavit, is a true and accurate copy of an article titled "Bytecode verification on Java smart cards" (the "Article").
- The author of the Article is listed as Xavier Leroy, a named inventor in the subject application.

- The Article was published in the year 2002 in Software: Practice & Experience, a peer-reviewed journal in the computer science industry.
- The applicant of the subject application maintains that the article is based on the invention claimed in the subject application.

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